

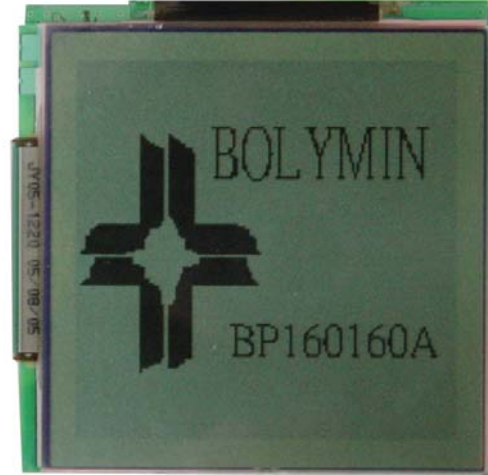
BP160160A

Feature

1. FSTN LCD, TAB IC, TCP package
2. No built-in controller, recommend SED1335
3. +5V power supply
4. 4-Bit data bus
5. 1/160 duty cycle
6. Option: LED B/L EL B/L & EL inverter
7. Option: Positive Voltage For LCD
8. Option: T/P, T/P decoder

Mechanical Data

Item	Standard Value	Unit
Module Dimension	69.0 x 69.5	mm
Viewing Area	60.1 x 60.0	mm
Dot Size	0.335 x 0.335	mm
Dot Pitch	0.35 x 0.35	mm



Pin Assignment

Pin	Symbol	Function
1~4	D0~D3	Data bus line (4 bits)
5	/DISPOFF	H:Display ON(default) L:Display OFF
6	FLM	First Line Marker
7	NC(M)	No connection(M signal input while not built-in circuit)
8	LP	Data latch pulse
9	CP	Clock signal
10	Vdd	Power supply for Logic(+5V)
11	Vss	Power supply (GND)
12	Vlcd	Positive voltage output +25V
13	Vadj	Contrast adjustment input (Vadj-Vss=LCD driving voltage)
14	LED_ON	LED on/
15	SK*/X1**	Serial clock / Right signal in Y axis
16	DO*/X2**	Datd output / Left signal in X axis
17	DJ*/Y1**	Data input / lower signal in Y axis
18	CS*/Y2**	Chip select / upper signal in Y axis
19	INT	Interrupt signal for Touch Panl controller
20	NC	No connection

Electronic Characteristics

Item	Symbol	Condition	Typical Value	Unit
Input Voltage	Vdd	Vdd = +3V	5.0	V
Supply Current	Idd	Vdd = +3V	30	mA
LCD Driving Voltage	Vadj-Vss	25°C	17	V
Edge LED Forward Voltage	Vt	25°C	3.2	V
Edge LED Forward Current	It	25°C	60	mA

Dimension

